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Dated: May 28, 2004

Signature:

Docket No.: SCEI 3.0-058  
(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Suzuoki et al.

Application No.: 09/816,752

Group Art Unit: 2154

Filed: March 22, 2001

Examiner: J. A. Follansbee

For: PROCESSING MODULES FOR COMPUTER  
ARCHITECTURE FOR BROADBAND  
NETWORKS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

### INFORMATION DISCLOSURE STATEMENT

Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed.

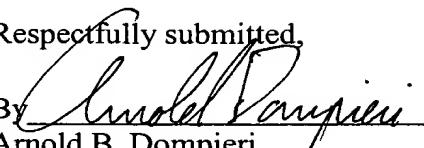
This application is related to U.S. patent application numbers 09/816,004 filed March 22, 2001; 09/815,554 filed March 22, 2001; 09/816,020 (now U.S. patent number 6,526,491); 10/371,322 filed February 21, 2003; and 09/815,558 filed March 22, 2001. The listed references were cited in one or more of these related applications.

Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: May 28, 2004

Respectfully submitted,

By 

Arnold B. Dompieri

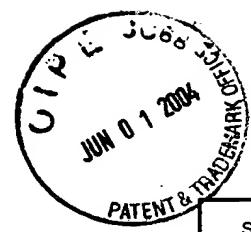
Registration No.: 29,736

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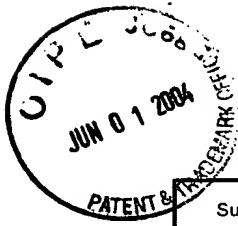


PTO/SB/08a/b (08-03)  
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Substitute for form 1449A/B/PTO				<b>Complete if Known</b>	
				Application Number	09/816,752
				Filing Date	March 22, 2001
				First Named Inventor	Masakazu Suzuki
				Art Unit	2154
				Examiner Name	J. A. Follansbee
Sheet	1	of	2	Attorney Docket Number	SCEI 3.0-058

<b>U.S. PATENT DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
AA	US-2002/0078285-A1	06-20-2002	Hofstee, et al.		
AB	US-2002/0078308-A1	06-20-2002	Altman, et al.		
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<b>FOREIGN PATENT DOCUMENTS</b>					
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)			
BA	EP-0 461 926-A2		12-18-1991	Compaq Computer Corporation	*
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BI	JP-08-180018		07-12-1996	Toshiba Corp.	*
BJ	JP-08-212178		08-20-1996	Hitachi, Ltd.	*
BK	JP-08-249261		09-27-1996	Mitsubishi Electric Corporation	*
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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

(Use as many sheets as necessary)

				<i>Complete if Known</i>
Application Number				09/816,752
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First Named Inventor				Masakazu Suzuki
Art Unit				2154
Examiner Name				J. A. Follansbee
Sheet	2	of	2	Attorney Docket Number
				SCEI 3.0-058

	BT	JP-2000-222384	08-11-2000	Yamatake Corp	*
	BU	JP-2002-032218	01-31-2002	Rikogaku Shinkokai	*
	BV	JP-54-012643	01-30-1979	Hitachi Ltd.	*
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	BX	JP-57-006952	01-13-1982	Nippon Telegram & Telephone Corp.	*
	BY	JP-64-012364	01-17-1989	Nippon Telegram & Telephone Corp.	*
	BZ	JP-64-023342	01-26-1989	Mitsubishi Electric Corp.	*

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
	CA	MASAKAZU SUZUOKI, Playstation 2 Yur Maikuropurosesssesse Emotion Engine (Microprocessor Emotion Engine for PlayStation 2), bit, Kyoritsu Shuppan Col, Ltd., 01-01-2000, pages 11-18, Vol. 32, No. 1.			
	CB	Baer, et al. "On the Inclusion Properties for Multi-Level Cache Hierarchies," Proceedings of the Annual International Symposium on Computer Architecture, Honolulu, Hawaii, May 30 - June 2, 1988, Washington, IEEE Computer Society Press, pp. 73-80.			
	CC	Mamoru Maekawa, et al., "Distributed Operating System -- That Coming Next to UNIX," 1st Ed., Dec. 25, 1991, Kyoritsu Shuppan Co., Ltd.			✓
	CD	"IBM System/370 Extended Architecture Instruction Manual," 1st Ed., IBM Japan, April 1984, pp. 3-8.			✓
	CE	William J. Dally, et al., "The Message-Driven Processor: A Multicomputer Processing Node with Efficient Mechanisms," IEEE Micro, April, 1992, pp. 23-39.			
	CF	Ted Painter, et al., "Perceptual Coding of Digital Audio," Proceedings of the IEEE, Vol. 88, No. 4, Apr. 2000, pp. 451-513.			

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<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Examiner Signature		Date Considered	
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\* Translation of Abstract only.

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